

0988365-062201

IMPROVED REACTOR WITH HEATED AND TEXTURED
ELECTRODES AND SURFACES

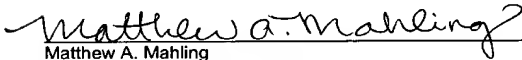
INVENTORS

Stephen P. DeOrnellas
Leslie G. Jerde
Kurt A. Olson

**CERTIFICATE OF MAILING BY "EXPRESS MAIL"
UNDER 37 C.F.R. § 1.10**

"Express Mail" mailing label number: **EL 670 728 239 US**
Date of Mailing: June 22, 2001

I hereby certify that this correspondence is being deposited with the United States Postal Service, utilizing the "Express Mail Post Office to Addressee" service addressed to **Box PATENT APPLICATION, Assistant Commissioner for Patents, Washington, D.C. 20231** and mailed on the above Date of Mailing with the above "Express Mail" mailing label number.


Matthew A. Mahling
Signature Date: June 22, 2001

IMPROVED REACTOR WITH HEATED AND TEXTURED ELECTRODES AND SURFACES

Inventors: Stephen P. DeOrnellas
Leslie G. Jerde
Kurt A. Olson

5

THS
W

10

Cross-Reference:

15

09880365-062201

20

[0001] Cross-referenced and incorporated by reference are pending U.S. Patent Applications entitled A METHOD FOR MINIMIZING THE CRITICAL DIMENSION GROWTH OF A FEATURE ON A SEMICONDUCTOR WAFER filed on November 19, 1997, and with Serial No. 08/974,089; and pending U.S. Patent Application entitled PLASMA REACTOR WITH A DEPOSITION SHIELD, filed on December 5, 1997, and with Serial No. 08/985,730; and pending U.S. Patent Application entitled PLASMA REACTOR WITH A DEPOSITION SHIELD filed on December 1, 1998, and with Serial No. 09/204,020.

25

Field of the Invention:

[0002] The present invention is directed to reactors and in particular reactors for processing objects with films such as semiconductor wafers.

Background of the Invention:

[0003] During fabrication of semiconductor chips, wafers are processed in reactors which can accomplish various steps associated with the definition of the functionality of the semiconductor chip. Such reactors can, for example, perform deposition and etching processes through the use of various gases which are part of the fabrication process. During the etch process, by way of example, gaseous

input materials as well as materials from the substrate being etched and combinations thereof can be deposited on the internal surface present in the reactor itself. Such surfaces include reactor walls, reactor electrodes, the reactor chuck and the like. Each processing tool, depending on the processing run, will have a regular scheduled down period during which the internal surfaces of the reactor will be cleaned and parts such as electrodes will be repaired and/or replaced.

[0004] The deposits which form on the various surfaces found in the reactor are known to have a detrimental effect to the fabrication of the semiconductor wafer product in the reactor. By way of example only, such deposits and layers on surfaces of the reactor can be thick and have poor reactor surface adhesion qualities. Additionally, the deposits or layers may not be very durable. All this potentially leads to the flaking or spaulding off of portions of the deposits or layers from the surfaces of the reactor. Such flaking or spaulding can interfere with the uniform processing of the surface of the wafer. For example, materials which flake or spauld from the surface of a reactor can redeposit on the surface of the wafer being processed, potentially damaging the functionality being fabricated on the wafer.

Summary of the Invention:

[0005] The present invention is directed to overcoming the problems associated with prior reactors. The present invention includes apparatus and method for ensuring that any materials that are deposited on the internal surfaces of the reactor are thin and more durable than those deposited by prior art reactors and that such deposits adhere more readily to the internal surfaces of the reactor.

[0006] Accordingly, it is an object of the invention to provide an apparatus and method which ensures that any layers or deposits on the internal surfaces of the reactor are thin, durable and adhere well to the surface of the reactor so that the deposits do not flake or spauld, potentially interfering with the process of defining the various layers which are being fabricated on a wafer.

[0007] It is to be understood that embodiments of the present invention can be used in the construction and fabrication of semiconductor chips as well as in the construction and fabrication of any other product. Such other products can include thin film read/write heads for a disk drive which requires the fabrication of circuitry on a substrate or which requires the fabrication of layers. Generally any construct having layers with features of submicron dimensions can benefit from the present invention.

[0008] Accordingly, it is another object of the present invention to provide reactor when the various surfaces of the reactor are heated to a temperature above that which would normally occur in the reactor in order to ensure that any materials deposited on the surface adhere well to the surface.

[0009] A further object of the present invention is to provide one or more electrodes, in particular the top electrode, of a reactor with a heater in order to ensure that the deposits and materials on the electrode adhere well to the electrode.

[0010] Still a further object of the invention is to provide the reactor with a reactor chamber and surfaces which are textured in order to encourage adherence of materials deposited thereon so that such materials do not flake or spauld off, interfering with the processing of a wafer.

[0011] In particular, it is an object of the present invention to provide an electrode and principally a top electrode of such a reactor with a textured surface in order to ensure that any material deposited thereon adheres to the reactor.

[0012] It is a further object of the present invention to provide a deposition shield which can be at least one of heated and/or textured in order to ensure that any materials deposited thereon adhere to the surface and do not flake or spauld off, potentially interfering with the processing of the substrate.

[0013] It is yet a further aspect of the present invention to coat the internal surfaces of the reactor with adhesion promoters which encourage the development of durable deposits which are less likely to flake or spauld and thus interfere with the processing of the work piece.

[0014] Further aspects and objects of the invention can be obtained from a review of the detailed description of the invention, the figures and the claims.

Brief Description of the Figures:

[0015] Fig. 1 is a schematical plan sectional view of an electrode with heaters.

[0016] Fig. 2 is a cross sectional view of the electrode of Fig. 1.

[0017] Figs. 3a, 3b, and 3c, are cross-sections of textured surfaces of embodiments of the invention.

[0018] Fig. 4 is a cross-section of another textured surface of an embodiment of the invention.

[0019] Fig. 5 is a cross-section of still a further textured surface of an embodiment of the invention.

[0020] Fig. 6 is a graph depicting a reduction in deposit thickness and halogen content as the electrode temperature is increased.

[0021] Fig. 7 is a side sectional view of a reactor surface which has been precoated.

[0022] Fig. 8 is a side view of a reactor with a shield protecting a reactor surface such as an electrode.

Detailed Description of the Preferred Embodiment:

Embodiment With a Heated Upper, Lower and Side Electrodes and Other Heated Surfaces:

[0023] Reactor embodiment of the present invention can include heated electrode, deposition shield and/or other surfaces. By way of example only, Figs. 1 and 2 depict plan and cross-sectional views of an upper electrode 20 for a reactor, and in particular of an etch reactor. The upper electrode has bores 22 provided therein which can receive heating elements 24. In this particular embodiment, two of the heating elements 24 are preferably cartridge heaters with an internal thermocouple. The third element 26 is a cartridge heater used as a thermocouple in order to sense the temperature. The cartridge heaters 24 and the cartridge heater 26 using a thermocouple are connected to a controller box 28 which uses the sensed temperature to maintain the level of heat generated by the cartridge heaters and thus the temperature of the upper electrode. In this preferred embodiment, the heaters are resistive type heaters. It is to be understood that other heaters can be employed and be within the spirit and scope of the invention. It is to be understood that the same technique can be used to heat electrode shields, and other surfaces and walls of the reactor.

5 [0024] In a preferred embodiment, where the electrode is made out of aluminum, the upper electrode (and/or the electrode opposite to the electrode or chuck holding a wafer to be processed) is preferably heated to a maximum temperature of about 300 to about 350 degrees C. With the upper electrode made of graphite or silicon, the maximum temperature is preferably about 400 to about 500 degrees C.

[0025] Without being so heated, in a typical etch reactor, the upper electrode would be floating at a maximum temperature of about 100 degrees C.

10 [0026] In a typical reactor, during the processing of a substrate such as a semiconductor wafer, reaction gases, materials from the wafer and combination thereof can be deposited on the various internal surface of the reactor and chamber, such as for example, the electrode. With the heated electrode of the above embodiment, the deposits are thinner, more adherent and more durable than is experienced when such reaction materials are deposited on non-heated surfaces. More particularly, when reactors and in particular etch reactor process substrate with platinum, the deposited layer on the electrode and other surfaces is more likely to be mostly platinum and not combinations of platinum with other gases such as chlorine and oxygen. Such other gases de-absorb or boil off from the surfaces in order to leave a more thin, durable and adhesive platinum layer. This layer accordingly sticks better to the surface of the electrode and does not easily flake or spauld off. Accordingly, there is less of a likelihood that any materials deposited on the electrode will flake off from the electrode and ruin the substrate being processed.

20 [0027] It has been found that by using heated upper electrodes, that there can be a reduction of seven-fold and even greater in the deposit thickness of

materials on the upper eductor. A stronger interfaced between the surface and the deposit is also experienced.

5 **[0028]** The graph of Fig. 6 demonstrates that as the temperature of the surface is increased, the deposit thickness greatly decreases along with the content of halogens (for halogen gas system) in the deposited material. With a decrease in halogen compounds (such as PtCl_x), the deposited materials are thinner and more adherent and tend toward a simple material such as platinum.

10 **[0029]** It is to be understood that although the above embodiment was discussed with respect to a heated upper electrode, that other surfaces present inside the reactor chamber can be also heated in order to accomplish the same benefit with respect to that surface. Further, it is to be understood that shields can be used to protect the electrode and other components, which shields are consumable and which shields can be heated and benefit from the present invention.

15 **[0030]** It is also to be understood that the present invention is most useful for etching non-volatile material such as, by way of example only, platinum (Pt), Iridium (Ir), barium strontium titanate (BST), lead zirconium titanate (PZT), bismuth strontium tantalate (SBT), Iridium Oxide (IrO_2), Titanium Nitride (TiN), and other non-volatile materials.

20 **[0031]** Other heating techniques, such as through the use of lamps to heat electrode and surfaces can be employed in order to enjoy the benefit of the present invention.

25 **Embodiment With a Textured Upper, Lower and Side
Electrodes and Other Textured Surfaces:**

5 [0032] In another preferred embodiment of the invention, the upper electrode and for that matter, other surfaces that are inside of the reaction chamber, can be textured such that layers that are deposited thereon have less likelihood of flaking or spaulding off, potentially contaminating the reaction. Such structures are particularly useful for etching non-volatile materials as described hereinabove. Such surface texturing promotes adhering of the deposits to the surface. Texturing can be as effective with capacitively coupled reactors. Further, inductively coupled reactors can also benefit from a texturing technique.

10 [0033] Texturing can take a variety of shapes and forms, both regular and irregular. The Figs. 3, 4, and 5 demonstrate several different representative embodiments for texturing. The first embodiments (Figs. 3a, 3b, and 3c) show surfaces 30, 32, 34 which are scalloped. The scallops presented are convex in shape toward the reactor chamber. Alternating the scallops can be concave toward the reactor chamber much as presented in Fig. 5. Such surface can be provided on the electrodes, shields for the electrode, and also on the various surfaces found inside of a reactor chamber.

15 [0034] Additionally, the embodiments shown in Figs. 4 and 5 include texturing which have a series of peaks 40, 50. The effect of this texturing in some instances can be measured by the aspect ratio of the width between peaks to the depth of the valley between the peaks. Thus the aspect ratio would be:

$$\frac{W}{D}$$

20 With a relatively low aspect ratio, in other words, with the width much less than the depth, it can be expected that such a textured surface would be better able to capture any deposited material than a texture where the width between peaks is

much greater than the depth. Additionally, a textured surface also increases the surface area upon which materials can be deposited and collected.

[0035] This embodiment with the textured surfaces as well as the embodiment with the heated electrode as indicated above prevent flaking, spaulding, delaminating, cracking and the accumulation of dust, all of which can interfere with the wafer fabrication process.

Embodiment With Pre-Coated Surfaces:

[0036] Another embodiment (Fig. 7) of the invention for use especially with such non-volatile films includes the pre-coating of the various surfaces 55 of the reaction chamber in order to promote chemical adhesion. Such pre-coating 60 can be done with Titanium (Ti) or Titanium Nitride (TiN). The surface found in the reactor chamber can be pre-coated with materials which are the same as or compatible with the non-volatile films which are being etched. Such materials can also include Platinum (Pt), Iridium (Ir), Iridium Oxide (IrO₂), Barium Strontium Titanate (BST), Strontium Bismuth Tantalate (SBT), Strontium Titanate (STO), Ruthenium (Ru), Ruthenium Oxide (RuO₂), and Lead Zirconium Titanate (PZT).

[0037] Additionally, the sidewalls of the reactor and in particular the sidewalls of the reactor liners can be provided with a matt finish which also promotes good chemical adhesion. A matt finish is defined as follows: General texturing of a surface to facilitate the adhesion of various materials. The matting is constructed in such a way as to maximize the surface area penetrated while minimizing spaulding of the deposited film.

[0038] As with the embodiments with the heated electrode and with the textured electrode, coating or pre-conditioning internal surfaces of the reactor chamber can prevent spaulding, flaking, and delaminating of any materials

deposited on the surfaces, beneficially effecting the processing of semiconducting wafers or other substrates

[0039] Fig. 8 shows a shield 70 which has been located adjacent to an upper electrode 80. The shield can either be heated, textured, or pre-coated, or a combination of the above and be within the spirit and scope of the invention. Texturing can include grooves, channels, perforations and/or screened surfaces.

Industrial Applicability:

[0040] The present invention advantageously uses heated and/or textured and/or pre-coated surfaces that are internal to a reactor chamber in order to ensure that materials deposited thereon adhere and do not flake, spall or become delaminated, contaminating the process.

[0041] Other advantageous, objects and aspects of the invention can be obtained from a review of the figures and the claims.

[0042] It is to be understood that other embodiments of the invention can be developed and fall within the spirit and scope of the invention and claims. It is also to be understood that all the above embodiments described with respect to semiconductor processing can be utilized for other techniques and in other reactors where there is a requirement that materials deposited on non-workpiece surfaces adhere so that they do not delaminate and contaminate the process.